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APPLICANTS

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** CONTINUING DATA ***** None A/K/S

** FOREIGN APPLICATIONS ***** None A/K/S

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged Examiner's Signature: <i>Asile Kumar Sarthar</i> Initials: <i>AS</i>	STATE OR COUNTRY SINGAPORE	SHEETS DRAWING 2	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 2
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TITLE
 Method of forming a relaxed semiconductor buffer layer on a substrate with a large lattice mismatch

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